

EXPRESS MAIL LABEL NO. EV314046062US

DATED: 16 JULY 2003

ATTY DOCKET: MXIC 1532-1

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of Inventor:

**Chih Yuan Huang et al.**

Application No.: \_\_\_\_\_

Filed: 16 July 2003 (herewith)

Title: **Method for Forming Silicon Film with Changing Grain Size by Thermal Process**

Group Art Unit: **unknown**

Examiner: **unknown**

**Customer No. 22470**

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.56**

Sir:

It is requested that the information identified in this statement be considered by the Examiner and made of record in the above-identified application. This statement is not intended to represent that a search has been made or that the information cited in the statement is, or is considered to be, material to patentability as defined in 37 C.F.R. §1.56.

***Enclosed with this statement are the following:***

- ☒ Form PTO-SB/08. The Examiner is requested to initial the form and return it to the undersigned in accordance with M.P.E.P. §609.
- ☒ A copy of each cited document as required by 37 C.F.R. §1.98.

***This statement should be considered because:***

This statement qualifies under 37 C.F.R. §1.97, subsection (b) because it is being filed within three months of the filing date of an application other than a continued prosecution application under § 1.53(d);

The Commissioner is hereby authorized to charge underpayment of any additional fees or credit any overpayment associated with this communication to Deposit Account No. 50-0869 (MXIC 1532-1). A duplicate copy of this authorization is enclosed.

Respectfully submitted,

HAYNES BEFFEL & WOLFELD LLP

Date: 7/16/2003

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Substitute for form 1449A/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  <i>(use as many sheets as necessary)</i>			<b>Complete if Known</b> <table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 50%;">Application Number</td> <td></td> </tr> <tr> <td>Filing Date</td> <td>16 July 2003 (herewith)</td> </tr> <tr> <td>First Named Inventor</td> <td>Chih Yuan Huang et al.</td> </tr> <tr> <td>Group Art Unit</td> <td></td> </tr> <tr> <td>Examiner Name</td> <td></td> </tr> <tr> <td>Attorney Docket Number</td> <td>MXIC 1532-1</td> </tr> </table>		Application Number		Filing Date	16 July 2003 (herewith)	First Named Inventor	Chih Yuan Huang et al.	Group Art Unit		Examiner Name		Attorney Docket Number	MXIC 1532-1
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Sheet	1	of	3													

U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code <sup>2</sup> (if known)			
	A1	4,963,506		Liaw et al.	10-16-1990	
	A2	5,751,637		Chen et al.	05-12-1998	
	A3	5,753,134		Biebl	05/19/1998	
	A4	5,874,129		Beinglass et al.	02-23-1999	
	A5	6,268,068		Heuer et al.	07-31-2001	
	A6	6,373,092		Okumoto	04-16-2002	
	A7	10/180,168		Luoh	06-26-2002	(filing date)
	A8					
	A9					
	A10					
	A11					
	A12					
	A13					
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FOREIGN PATENT DOCUMENTS								
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		Office <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>5</sup> (If known)				
	B1							
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<sup>1</sup> Unique citation designation number. <sup>2</sup> See attached Kinds of U.S. Patent Documents. <sup>3</sup> Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>6</sup> Applicant is to place a check mark here if English language Translation is attached.

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3

**Application Number**

Filing Date

16 July 2003 (herewith)

**First Named Inventor**

Chih Yuan Huang et al.

Group Art Unit

Examiner Name \_\_\_\_\_

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**OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS**

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OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.		T <sup>2</sup>
	C1	KAHLER, U., et al. "Size related properties of silicon nanoparticles from decomposition of SiOx" <a href="http://www.mpi-halle.mpg.de/annual_reports/2000.pdf/jb_results_14.pdf">http://www.mpi-halle.mpg.de/annual_reports/2000.pdf/jb_results_14.pdf</a> , 2 pages.	/	
	C2	"Polysilicon: The Successor Technology," <a href="http://www.wtec.org/loyola/dsply_jp/c5_s5.htm">http://www.wtec.org/loyola/dsply_jp/c5_s5.htm</a> (June 1992), 3 pages	/	
	C3	Polysilicon TFT, The Future is clear with Toshiba, <a href="http://www.toshiba-europe.com/computers/tnt/tft/">http://www.toshiba-europe.com/computers/tnt/tft/</a> (2002), 1 page	/	
	C4	Higher Resolution, The Future is clear with Toshiba, <a href="http://www.toshiba-europe.com/computers/tnt/tft/resolution.htm">http://www.toshiba-europe.com/computers/tnt/tft/resolution.htm</a> (2002), 1 page	/	
	C5	Polysilicon Explained, The future is clear with Toshiba, <a href="http://www.toshiba-europe.com/computers/tnt/tft/explained.htm">http://www.toshiba-europe.com/computers/tnt/tft/explained.htm</a> (2002), 1 pate	/	
	C6	"Flash Memory Technology" Chapter 10, Integrated Circuit Engineering Corporation, <a href="http://smithsonianchips.si.edu/ice/cd/MEMORY97/SEC10.PDF">http://smithsonianchips.si.edu/ice/cd/MEMORY97/SEC10.PDF</a> (1997), pp. 10-1 through 10-16.	/	
	C7	WILSON, RON, "Flash gets a quantum makeover at IEDM," EETIMES (12/13/2002), 4 pages.	/	
	C8	"The K-Input Floating-Gate MOS (FGMOS) Transistor" <a href="http://people.ece.cornell.edu/minch/talks/FGMOSTdevicelayout.pdf">http://people.ece.cornell.edu/minch/talks/FGMOSTdevicelayout.pdf</a> , 22 pages	/	
	C9	DIPERT, BRIAN, "Exotic Memories, diverse approaches" <a href="http://www.ednmag.com">www.ednmag.com</a> (04/26/2001), 9 pages	/	
	C10	LAMMERS, DAVID, "Chip makers bang away at metal gates" EETIMES (11/26/2002), 3 pages	/	
	C11	"Characterizing Very Thin ONO Gates with Laser Spectroscopic Ellipsometry" Rudolph Technologies, Inc. Application Reports (2001) <a href="http://www.rudolphtech.com/reports/VeryThinONOGates.htm">http://www.rudolphtech.com/reports/VeryThinONOGates.htm</a> , 3 pages	/	

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